

# MPC5121e/3

MPC603e series e300 core

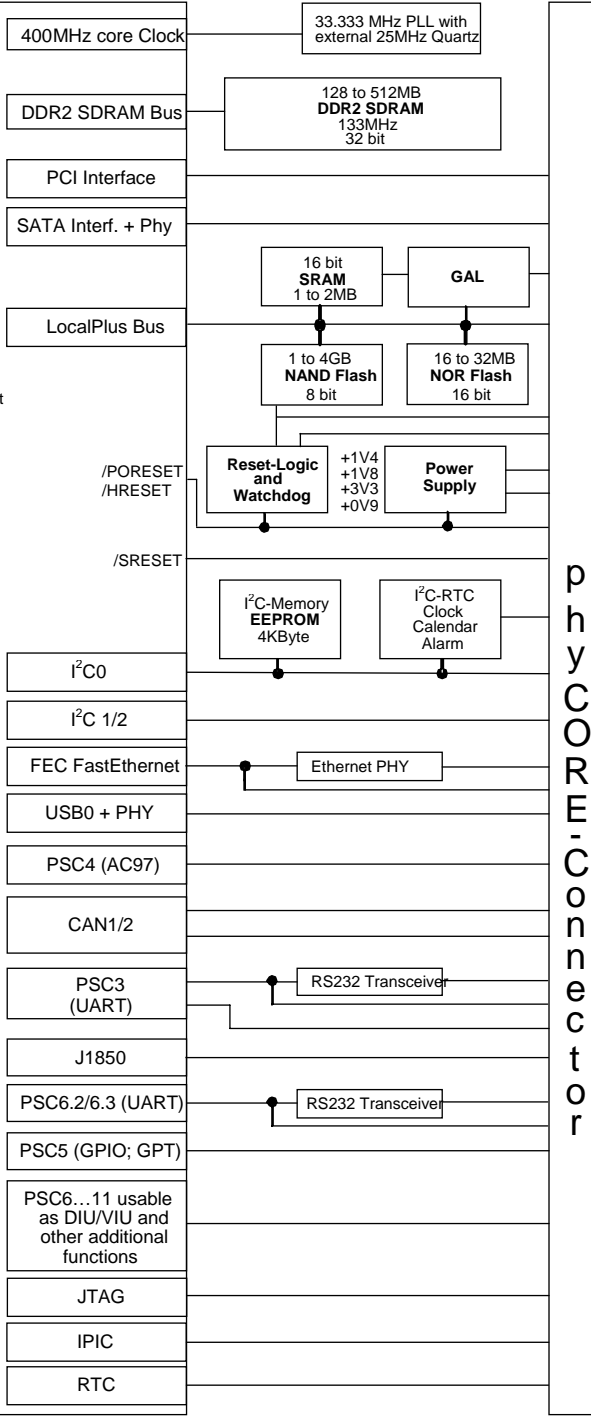
32k D-cache

32k I-cache

AXE Auxiliary Execution Engine

MMU Memor Management Unit

DMA I/O-Subsystem



PHY CONNECTOR

Separated PCI Interface Version 2.3

SATA compliant with SATA 1.0a

free usable I/O GAL\_FS0..4

LocalPlus Bus :  
External Bus Interface

/FLASH\_RESET  
Watchdog signals; Master Reset In

VBat +3V for RTC and SRAM  
Power +3V3@1.2A

/PORESET  
/HRESET  
/SRESET

/INT\_RTC; RTC\_CLK

i²C-Bus 0

i²C-Bus 1/2  
i²C1 alternatively usable as SPDIF

10/100 Mbit Ethernet  
MII Interface  
USB 2.0 OTG

PSC4 can be used as AC97 codec

CAN1 TTL  
CAN2 TTL

UART3 RXD3 / TXD3  
UART3 TTL  
SPI Bus

SAE J1850 Class B compliant

UART6 RXD6 / TXD6  
UART6 TTL

PSC5 multi function PIOs

PSC6...11 multi function PIOs

JTAG Debug-/Test Port

/IRQ0; /IRQ1

wakeup inputs