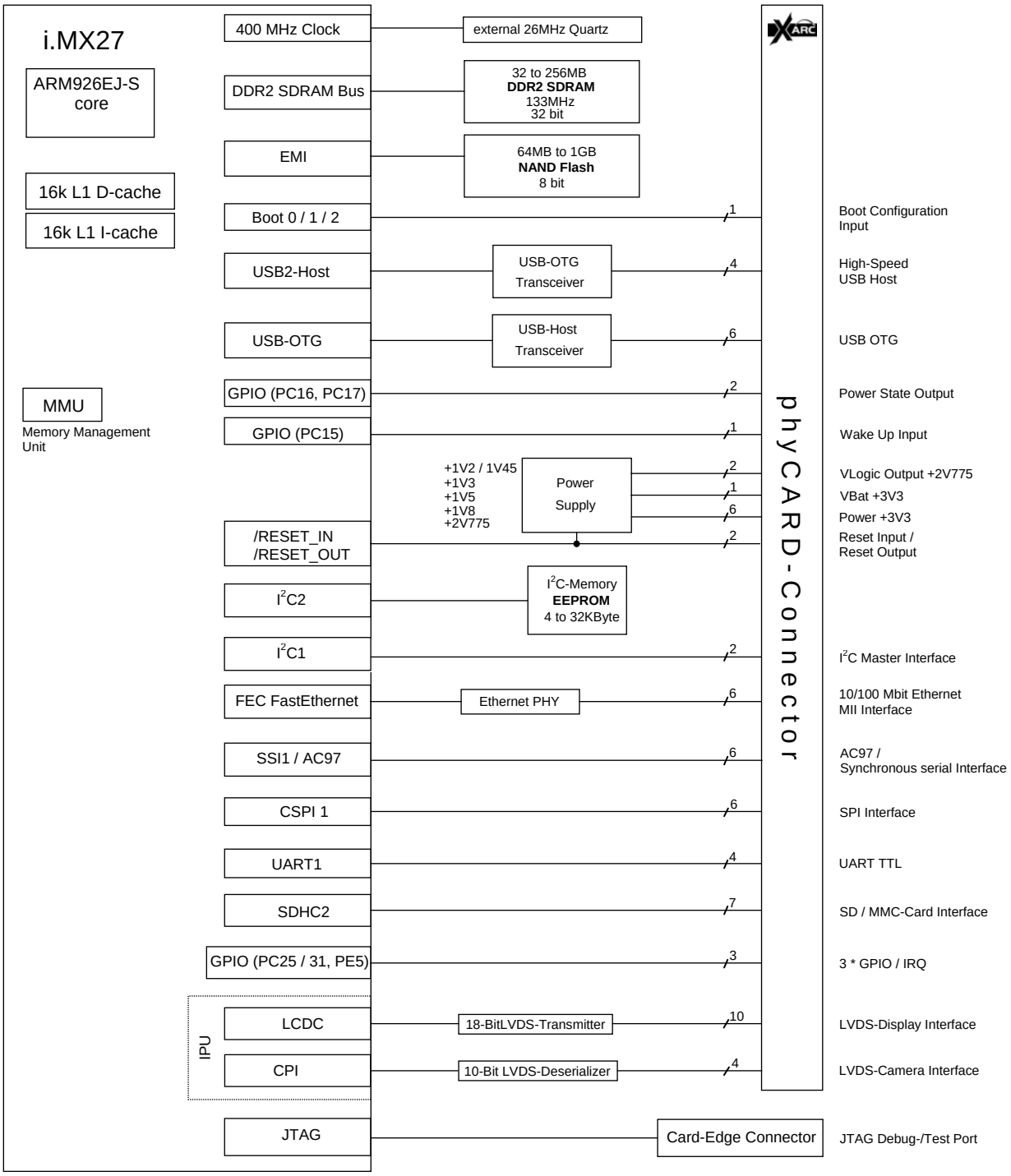
i.MX27
ARM926EJ-S core
16k L1 D-cache
16k L1 I-cache
MMU
Memory Management Unit
400 MHz Clock
external 26MHz Quartz
DDR2 SDRAM Bus
32 to 256MB
DDR2 SDRAM
133MHz
32 bit
EMI
64MB to 1GB
NAND Flash
8 bit
Boot 0 / 1 / 2
1
Boot Configuration Input
USB2-Host
USB-OTG Transceiver
4
High-Speed USB Host
USB-OTG
USB-Host Transceiver
6
USB OTG
GPIO (PC16, PC17)
2
Power State Output
GPIO (PC15)
1
Wake Up Input
+1V2 / 1V45
+1V3
+1V5
+1V8
+2V775
Power Supply
2
VLogic Output +2V775
1
VBat +3V3
6
Power +3V3
/RESET_IN
/RESET_OUT
2
Reset Input / Reset Output
I2C2
I2C-Memory
EEPROM
4 to 32KByte
I2C1
2
I2C Master Interface
FEC FastEthernet
Ethernet PHY
6
10/100 Mbit Ethernet MII Interface
SSI1 / AC97
6
AC97 / Synchronous serial Interface
CSPI 1
6
SPI Interface
UART1
4
UART TTL
SDHC2
7
SD / MMC-Card Interface
GPIO (PC25 / 31, PE5)
3
3 * GPIO / IRQ
IPU
LCDC
18-BitLVDS-Transmitter
10
LVDS-Display Interface
CPI
10-Bit LVDS-Deserializer
4
LVDS-Camera Interface
phyCARD-Connector
JTAG
Card-Edge Connector
JTAG Debug-/Test Port